



DRIVING CIRCUIT DESIGN GUIDE

FOR BACKLIGHT AND DC-DC CONVERTER

Rev.1.0
2022-09-05

This document is applied for the Riverdi HB, IPS, 7.0" RGB series:

PRODUCT NAME	DESCRIPTION
RVT70HSTNWN00	HB, IPS, 7.0", 1024x600, 1000cd/m ² , RGB, No touch panel,
RVT70HSTFWN00	HB, IPS, 7.0", 1024x600, 1000cd/m ² , RGB, No touch panel, Metal frame
RVT70HSTNWC00-B	HB, IPS, 7.0", 1024x600, 850cd/m ² , RGB, uxTouch, Optical bonding
RVT70HSTNWC00	HB, IPS, 7.0", 1024x600, 800cd/m ² , RGB, uxTouch, Air bonding
RVT70HSTNWCA0	HB, IPS, 7.0", 1024x600, 800cd/m ² , RGB, aTouch, Air bonding
RVT70HSTFWCA0	HB, IPS, 7.0", 1024x600, 800cd/m ² , RGB, aTouch, Air bonding, Metal frame



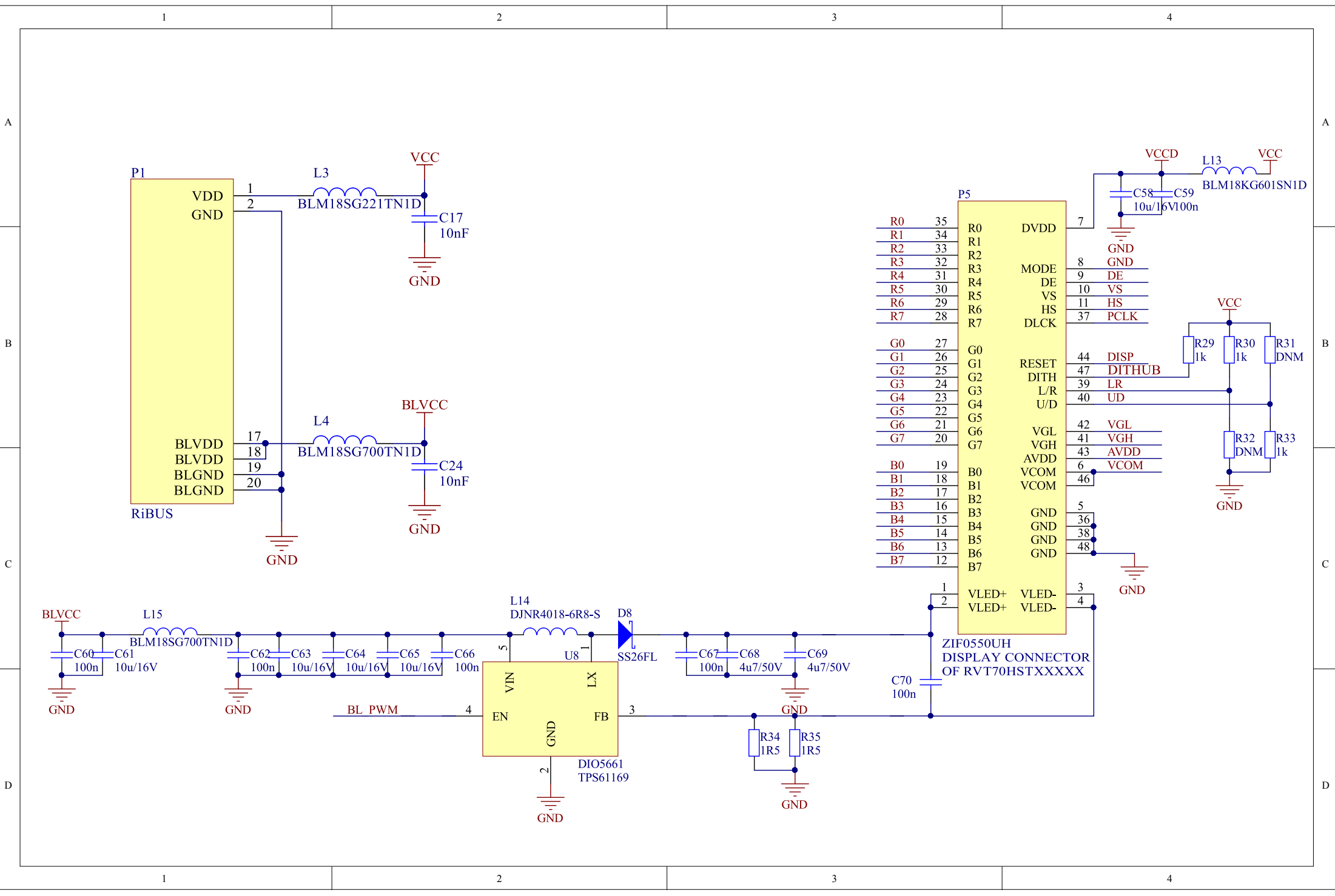
1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2022-09-05	Initial Release	



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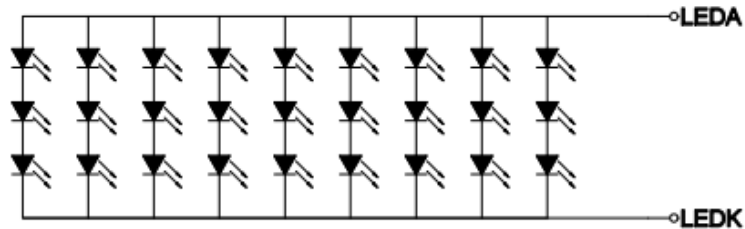


R0	35	R0	DVDD	7
R1	34	R1		
R2	33	R2		
R3	32	R3	MODE	8
R4	31	R4	DE	9
R5	30	R5	VS	10
R6	29	R6	HS	11
R7	28	R7	DLCK	37
G0	27	G0		
G1	26	G1	RESET	44
G2	25	G2	DITH	47
G3	24	G3	LR	39
G4	23	G4	L/R	40
G5	22	G5	U/D	
G6	21	G6	VGL	42
G7	20	G7	VGH	41
B0	19	B0	AVDD	43
B1	18	B1	VCOM	6
B2	17	B2	VCOM	46
B3	16	B3	GND	5
B4	15	B4	GND	36
B5	14	B5	GND	38
B6	13	B6	GND	48
B7	12	B7	GND	
VLED+	1	VLED-		
VLED+	2	VLED-		
VLED+	3	VLED-		
VLED+	4	VLED-		



3. BACKLIGHT DRIVING CIRCUIT

Internal backlight circuit of Riverdi HB, IPS 7.0" RGB Series is built with 9x3 (3LEDs in a row) LED matrix.



LED Diagram Circuit

Backlight parameters of Riverdi 7.0" HB, IPS series:

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Backlight Driving Voltage	V_F	9.0	9.6	10.2	V	
Backlight Driving Current	I_F	-	270	-	mA	
Backlight Power Consumption	W_{BL}	-	2592	-	mW	
LED Lifetime	-	-	50,000	-	hours	Note 1

To get the full brightness, the driving current (I_F) needs to reach 270 mA.

The recommended LED driver is TPS61169 or DIO566,

For example, the feedback voltage of TPS61169: $V_{FB} (max)=204mV$

In our design, R34, R35 connected in parallel as a feedback resistor (R_{FB}) is set to 0.75R to achieve the full brightness.

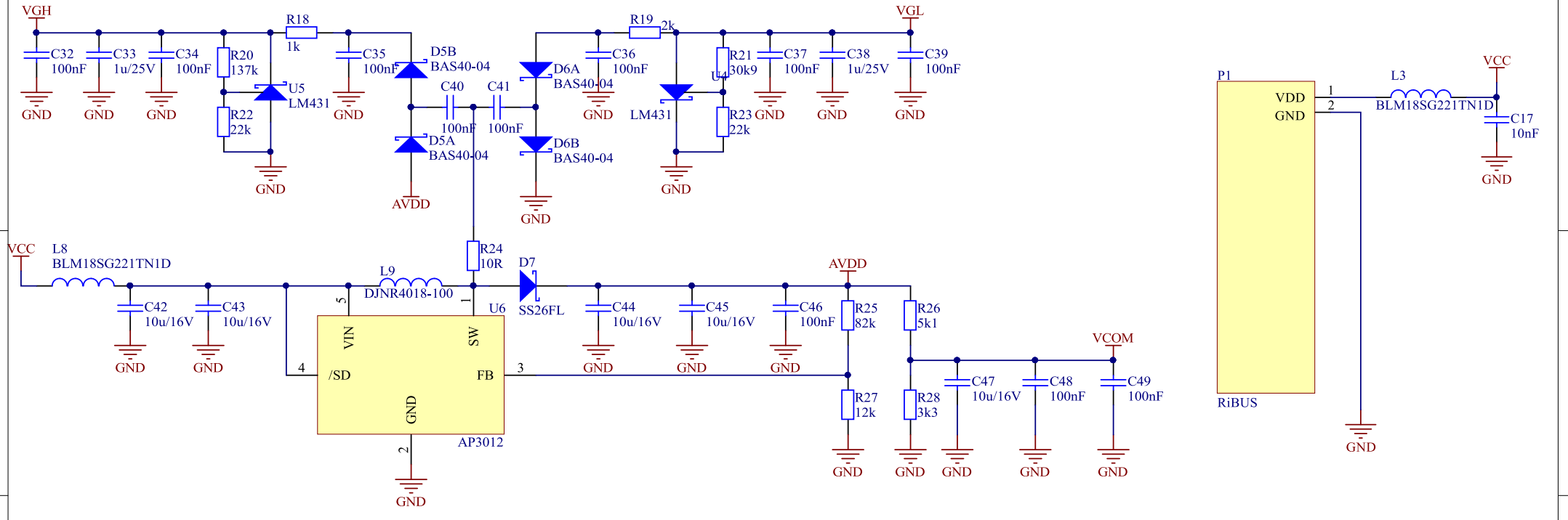
$$I_F (max)=V_{FB} (max)/ R_{FB}$$

$$I_F (max)=204mV/0.75R= 272 \text{ mA.}$$

Both TPS61169 and DIO5661 have PWM dimming control input to drive the LED current. TPS61169 and DIO5661 have built-in low-pass filter which changes internal feedback voltage. By that, inverter is not switched on-off with PMW signal but change LEDs current effectively in continuous way which cause low EMI emissions.

Please note that all Riverdi displays are designed to have low emission, that's why many LC components like beads and capacitors are on the schematic. They are not necessary but strongly recommended.

Recommended PMW frequency is 5kHz – 100kHz for TPS61169 or 200Hz - 200kHz for DIO5661.





4. DRIVING CIRCUIT OF VGH/VGL/ AVDD/VCOM

Riverdi HB, IPS, 7.0" RGB series TFT require VGH, VGL, AVDD and VCOM voltages to drive the TFT glass driver properly.

Below tables are the VGH, VGL, VCOM, and AVDD specifications for Riverdi HB, IPS, 7.0" RGB series.

Absolute maximum ratings:

PARAMETER	SYMBOL	MIN	MAX	UNIT
Power for Circuit Driving	VDD	-0.3	3.96	V
	AVDD	-0.5	14.85	
	VGH	-0.3	40	
	VGL	-20.0	0.3	

Electrical specifications:

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Module	VGH	17	18	19	V	
	VGL	-6.6	-6.0	-5.4		
	AVDD	9.4	9.6	9.8		
	VCOM	3.6	3.8	4.0		
Current of Power Supply	IAVDD	-	35	45	mA	AVDD = 9.6V
	IGH	-	0.5	1	uA	VGH = 18V
	IGL	-	0.5	1	mA	VGL= -6V

The schematic of VGH, VGL, AVDD and VCOM circuit with AP3012KTR-E1 DC-DC converter.



Hi, I am here to help you!
If you have any additional
questions, please contact
our support via email:
contact@riverdi.com

