TFT MODULE SPECIFICATION

RVT70HSSNWC00 V2.1A

STM32 EMBEDDED 7.0” display datasheet
Rev. 1.4
2026-02-13

Riverdi Sp. z o.o.

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ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	7.0	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	179.96 x 119.00 x 19.47	mm
Active Area (W x H)	154.21 x 85.92	mm
Pixel Pitch (W x H)	0.1506 x 0.1432	mm
Resolution	1024 x 600	/
Brightness	800	cd/m^2
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
Micro Controller Unit	STM32H757XIH6	/
External Flash Memory	512Mbit	/
Supply Voltage for Module	6.0 - 48.0	V
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Weight	TBD	g

Note 1. RoHS3 compliant

Note 2. LCM weight tolerance: ± 5%.

1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.1	2022-03-28	Preliminary version	
1.0	2022-07-12	Initial Release • Update the measurement data of electrical parameters • Update drawing with adding heatsink Adding photos of PCB board	
1.1	2022-12-15	New PCB version: 70STM32H7_Rev1.1 • MCU is switched from STM32H747XIH6 to STM32H757XIH6 • The maximum supply voltage is increased to 48.0V • Coin cell holder for CR1220 is added • New method to set BLVDD SELECTOR	
1.2	2023-08-23	Correct the name of pin 31 on expansion connector to PI8	
1.3	2024-07-04	Updated the PCB laminate to 1.3, for details refer to changelog.	
1.4	2026-02-13	Release of module V2.1A, updated the PCB laminate to 2.0, for details refer to changelog.	

2. CONTENTS

3. MODULE CLASSIFICATION INFORMATION

RV	T	70	H	S	S	N	W	C	00	V2.1A
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.	11.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	70 – 7.0”
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	S – 1024 x 600 px
6.	INTERFACE	S – STM32 Embedded Display MCU STM32H757XIH6
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – with Capacitive Touch Panel
10.	VERSION	00 – version
11.	MODULE REVISION	V2.1A

4. ASSEMBLY

4.1 uxTouch Assembly

uxTouch are LCD TFT displays with specially designed projected capacitive touch panels. uxTouch display can be mounted without any additional holes in the housing. Our standard uxTouch displays include double-sided adhesive tape (DST) to stick TFT easily to the housing.

uxTouch models with double-side adhesive tape can be mounted by fastening the glass to the housing.

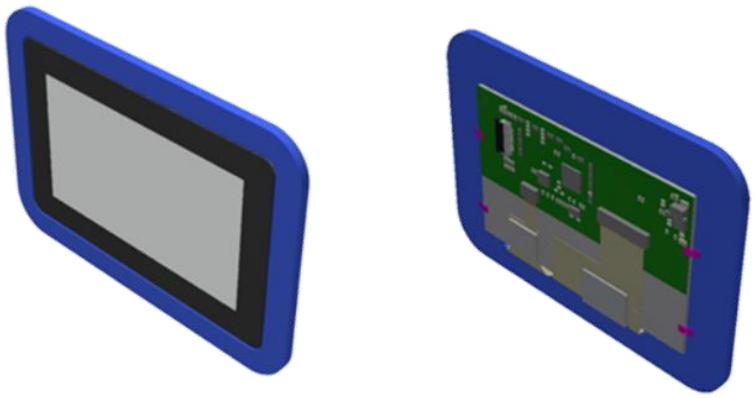
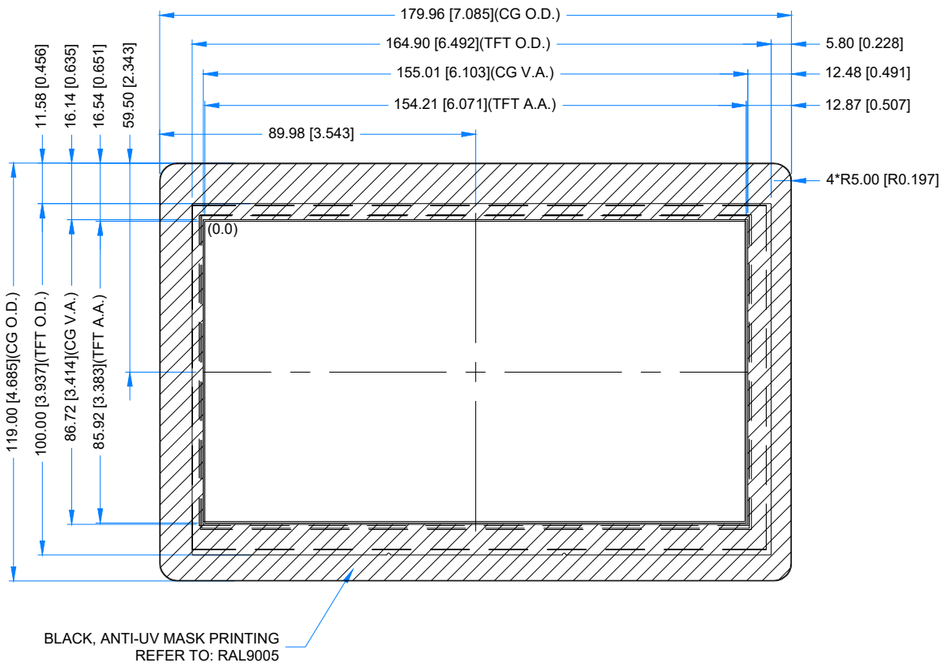
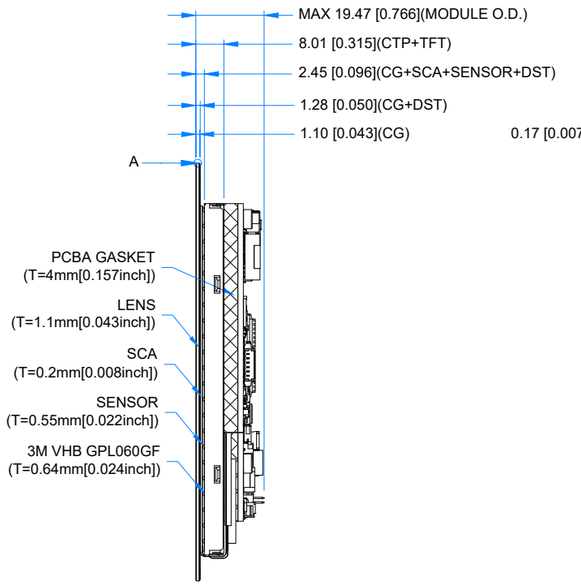
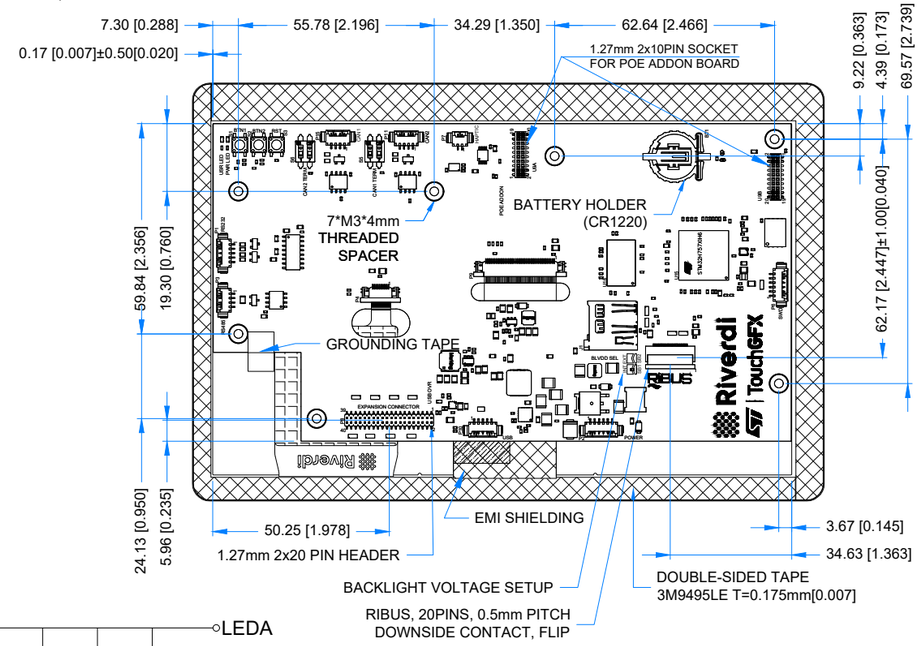


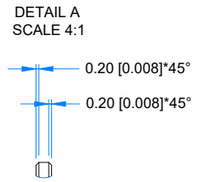
Figure 1. General view of the module

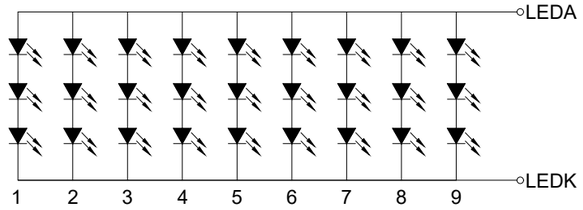
Revision:	Changes:	Date:
1.0	Initial Case	2026.01.12











LED Diagram Circuit

1.25 MM MOLEX CONNECTORS

NO.	TYPE	MATE WITH
P1 (RS232)	53261-0571	51021-0500
P2 (POWER)	53261-0671	51021-0600
P3 (RS485)	53261-0471	51021-0400
P6 (SWD)	53261-0671	51021-0600
P7 (HAPTIC)	53261-0271	51021-0200
P10 (USB)	53261-0571	51021-0500
P11 (CAN1)	53261-0471	51021-0400
P15 (CAN2)	53261-0471	51021-0400

1.27MM PIN HEADER AND SOCKET

NO.	TYPE	MATE WITH
P8	CH51402M100-0P	CBC1402D100 OR CBC1402M100-2P
U9a & U9b	PBHTD20SPP	CH51202V100 OR CH51202M100-0P

TFT NOTES:
1. DISPLAY TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
2. RESOLUTION: 1024x600
3. VIEWING ANGLE: FREE
4. SURFACE LUMINANCE: 800 cd/m^2
5. MCU ON THE BOARD: STM32H757XIH6
6. EXTERNAL FLASH MEMORY SIZE:512Mbit
7. EXTERNAL SDRAM SIZE:64Mbit (32-bit access)
8. SUPPLY VOLTAGE FOR MODULE: 6.0V - 48.0V

TP NOTES:
1. TP STRUCTURE: G+G
2. CG THICKNESS: 1.10mm[0.043inch]
3. SURFACE HARDNESS: 7H
4. DRIVER IC: ILI2132A
5. INTERFACE: CONNECTED TO MCU VIA I2C

GENERAL NOTES:
1. AIR BONDING
2. OPERATING TEMPERATURE: -20°C ~ 70°C
3. STORAGE TEMPERATURE: -30°C ~ 80°C
4. WITHOUT INDIVIDUAL TOLERANCE: ±0.3mm[0.012inch]
5. RoHS3 COMPLIANT

PN: RVT70HSSNWC00 V2.1A				
SN:				
DRAWN:	M.Stabinski	2026.01.12	1:1.75	
CHECKED:	C.Gao	2026.01.21	[mm]	
APPR:	M.Wierzbowski	2026.01.21	ISO A3	P. 1 of 1

6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTE
Supply Voltage for Module	VDD_IN	0.0	60.0	V	Note 1
Digital I/O signals Voltage	-	-0.5	3.3	V	Note 1,2
Operating Temperature	T_{OP}	-20	70	°C	
Storage Temperature	T_{ST}	-30	80	°C	
Storage Humidity (@ 25 ± 5°C)	H_{ST}	10	-	% RH	
Operating Ambient Humidity (@ 25 ± 5°C)	H_{OP}	10	-	% RH	

Note 1. Exceeding maximum values may cause improper operation or permanent damage to the unit.

Note 2. Most of the GPIOs have the 5.0 V tolerant input voltage, please refer to the datasheet of MCU for more details.

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Module	VDD_IN	6.0	12.0	48.0	V	
Power Enable Voltage	VEN	0	-	VDD_IN	V	
Input Voltage “H” Level	V_{IH}	2.0	-	3.3	V	
Input Voltage “L” Level	V_{IL}	0	-	0.8	V	

PARAMETER	CONDITION	SYMBOL	1% BL	50% BL	100% BL	UNIT
Current Drawn from VDD_IN @6.0V	Power 'Enable'='0'	I_{VDD_IN}	60			uA
Current Drawn from VDD_IN @12.0V			151			
Current Drawn from VDD_IN @24.0V			395			
Current Drawn from VDD_IN @36.0V			630			
Current Drawn from VDD_IN @48.0V			870			
Current Drawn from VDD_IN @6.0V	Power 'Enable' = '1'	I_{VDD_IN}	308	621	781	mA
Current Drawn from VDD_IN @12.0V			139.3	255	368	
Current Drawn from VDD_IN @24.0V			70	125	187	
Current Drawn from VDD_IN @36.0V			47	84	127	
Current Drawn from VDD_IN @48.0V			36	63.8	98	

Note 1. POWER 'ENABLE' refers to pin 4, 'ENABLE' of the power input connector(P10).

POWER 'ENABLE' = '1' is when EN pin is floating or shorted to VDD_IN.

POWER 'ENABLE' = '0' is when EN pin is shorted to GND.

By default, POWER 'ENABLE' is set to "1".

Note 2. Test condition: Ambient temp 25 °C PCAP is on Active mode.

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Lifetime	-	-	30,000	-	hours	Note 1

Note 1. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.

9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf	θ=0° ø=0° Ta=25 °C	-	27	45	ms	FIG 2.	4, 7
Contrast Ratio	Cr		600	800	-	---	FIG 3.	1, 7
Surface Luminance	Lv		-	800	-	cd/m2		2, 7
Unioformity	U	-	70	75	-	%	FIG 3.	3
Viewing Angle Range	θ	ø = 90°	80	85	-	deg	FIG 4.	6
		ø = 270°	80	85	-	deg		
		ø = 0°	80	85	-	deg		
		ø = 180°	80	85	-	deg		
CIE (x, y) Chromaticity	Rx	θ=0° ø=0° Ta=25 °C	0.581	0.611	0.641	-	FIG 3.	5,7
	Ry		0.295	0.325	0.355	-		
	Gx		0.306	0.336	0.366	-		
	Gy		0.513	0.543	0.573	-		
	Bx		0.118	0.148	0.178	-		
	By		0.127	0.157	0.187	-		

	Wx		0.291	0.321	0.351	-		
	Wy		0.315	0.345	0.375	-		
NTSC ratio	-	-	40	47	-	%		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 2.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 1. The test equipment is BM-7A.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. For TFT module, viewing angle is the angle at which the contrast ratio is greater 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 3.

Note 7. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

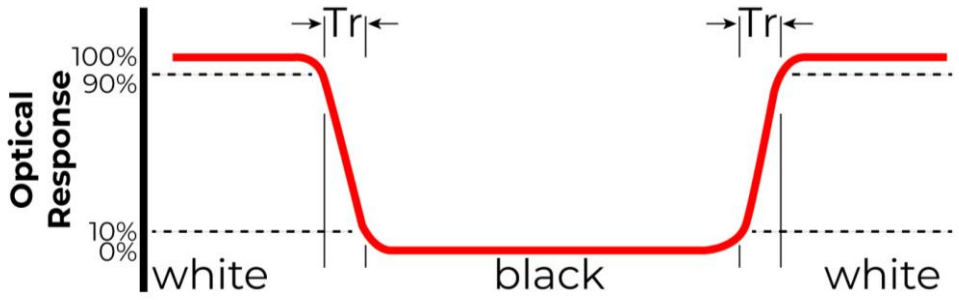
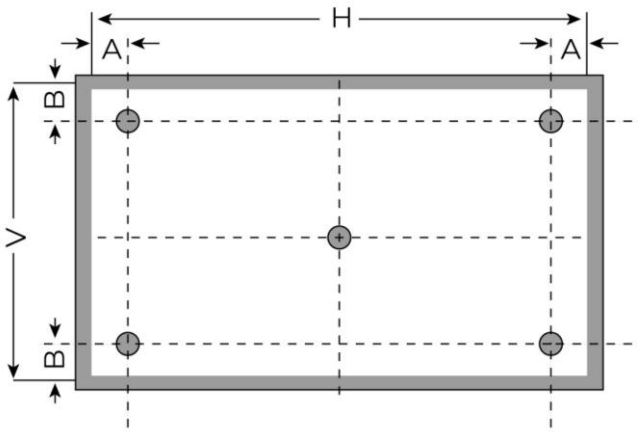


Figure 2. The definition of response time



A: 5mm

B: 5mm

H, V: Active Area

Light spot size ∅=5mm, 500mm distance from the LCD surface to detector lens.

Measurement instrument is SR-3A

Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

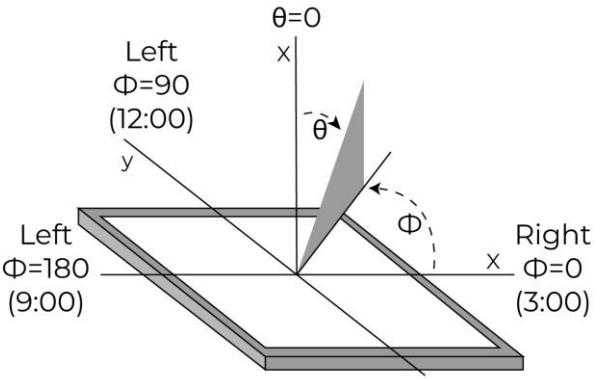
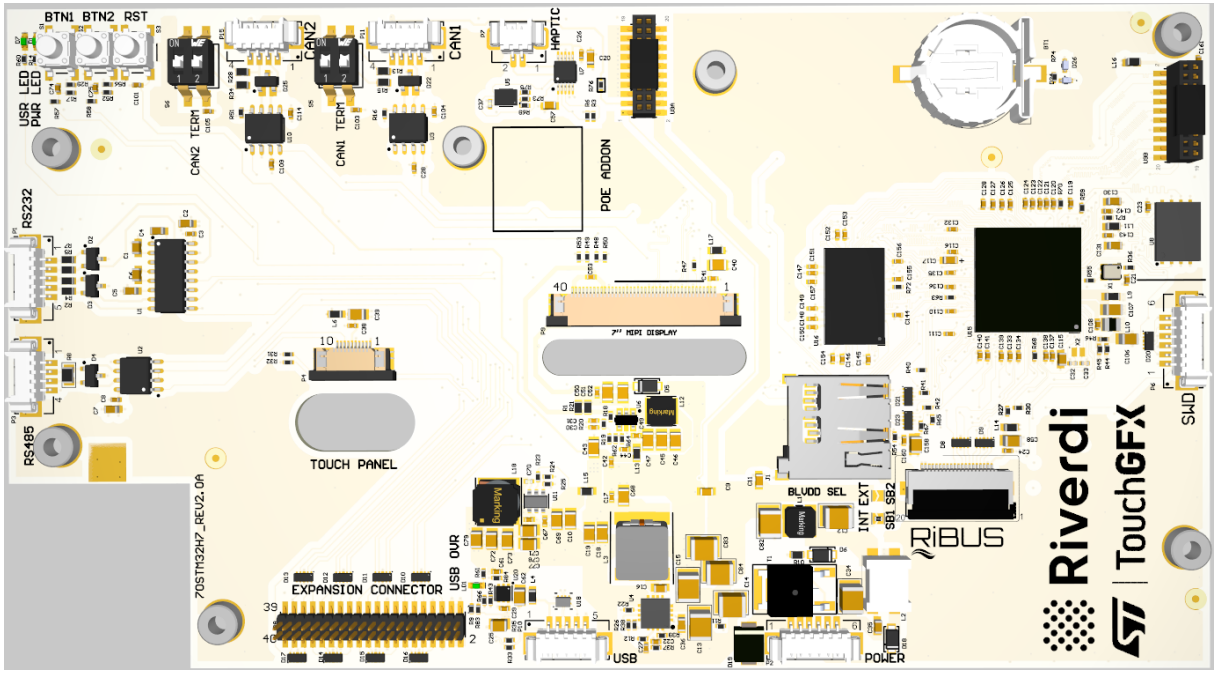


Figure 4. The definition of viewing angle

10. INTERFACES DESCRIPTION



10.1 Power input connector (P2)

The 1.25mm, 6-pin Molex connector labeled as “POWER’ (P2) is power input connector.

There is an internal reverse polarity protection which ensures that the device is not damaged if the power supply polarity is reversed.

PIN	SYMBOL	DESCRIPTION	NOTE
1	VDD_IN	Power supply input; 6.0-48.0V	Note 1
2	VDD_IN	Power supply input; 6.0-48.0V	
3	VDD_IN	Power supply input; 6.0-48.0V	
4	ENABLE	Enable/ Disable power supply	Note 2
5	GND	Ground	
6	GND	Ground	

Note 1. The STM32 Embedded Display system allows for the direct connection of an additional display through the RiBUS connector on the board. You can connect any intelligent display from Riverdi based on EVE4 (BT817Q). Please see chapter 10.8 for detailed info.

Note 2. The "ENABLE" pin is internally pulled up to VDD_IN.

To enable the device, please leave the EN pin unconnected or short it to the "VDD_IN" pin.

To disable the device, short the EN pin to GND.

Note 3. Matched Riverdi power supply cable: RVA-0106M-1.25FF-1.

10.2 USB interface (P10)

The 1.25mm, 5-pin Molex connector labeled as “USB’ (P10) is USB interface.

PIN	SYMBOL	DESCRIPTION	NOTE
1	VCC_USB	Power supply	Note 1
2	D-	USB data-	
3	D+	USB data+	
4	ID	USB OTG ID; Host /Device detect	Note 2
5	GND	Ground	

Note 1. The maximum current drawn from VCC_USB is 500mA.

Note 2. Configuration of the USB Host/Device mode:

Host Mode: Pin 4 (ID) should be connected to GND.

In this mode, it can provide +5V output voltage to the connected USB device and Max output current 500 mA.

Device Mode: Pin 4 (ID) should be not connected (floating).

Note 3. Matched Riverdi cable accessory: RVA-0105M-1.25FF

10.3 RS485 interface (P3)

The 1.25mm, 4-pin Molex connector labeled as “RS485’ (P3) is RS485 interface.

PIN	SYMBOL	DESCRIPTION	NOTE
1	VDD	Power supply output; 6.0-48.0V	Note 2
2	A	Non-inverting receiver input and non-inverting driver output	
3	B	Inverting receiver input and inverting driver output	
4	GND	Ground	

Note 1. Matched Riverdi cable accessory: RVA-0104M-1.25FF

Note 2. VDD is internally connected with Power connector VDD_IN pins. This is not used to power supply the board.

10.4 RS232 interface (P1)

The 1.25mm, 5-pin Molex connector labeled as “RS232’ (P1) is RS232 interface.

PIN	SYMBOL	DESCRIPTION	NOTE
1	RTS	Request to send	
2	CTS	Clear to send	
3	TXD	Transmit Data	Note 1, 3
4	RXD	Receive Data	Note 3
5	GND	Ground	

Note. Matched Riverdi cable accessory: RVA-0105M-1.25FF

10.5 2 x CAN FD interfaces (P11/P15)

The main board supports 2 channels of the CANFD (Control Area Network Flexible Data-Rate) communication bus, based on the high-speed (2.5-8.0Mbps) CAN transceiver.

2 pcs of 1.25mm, 4-pin Molex connectors labeled as “CAN1’ (P11) and “CAN2” (P15) are respectively interfaces of CAN FD1 and CAN FD2.

PIN	SYMBOL	DESCRIPTION	NOTE
1	GND	Ground	
2	CAN_L	CAN Low-Level Voltage	
3	CAN_H	CAN High-Level Voltage	
4	VDD	Power supply output; 6.0 - 48.0 V	Note 2

Note 1. Matched Riverdi cable accessory: RVA-0104M-1.25FF

Note 2. VDD is internally connected with Power connector VDD_IN pins. This is not used to power supply the board.

10.6 Haptic feedback connector

PIN	SYMBOL	DESCRIPTION	NOTE
1	OUT-	Negative haptic driver differential output	Note 1
2	OUT+	Positive haptic driver differential output	

Note 1. The haptic driver DRV2605L is controlled directly by I2C protocol.

Note 2. Matched Riverdi cable accessory: RVA-0102M-1.25FF

10.7 SWD (Serial Wire Debug) connector

PIN	SYMBOL	DESCRIPTION	NOTE
1	VCC_+3.3V	Power input	
2	SW_CLK	Serial wire clock	
3	GND	Ground	
4	SW_DIO	Serial wire debug data input/output	
5	RST	Reset; Active low	
6	SWO	Serial wire trace output	Note 2

Note 1. Riverdi ST-LINK/V2 SWD programing cable: RVA-SWD-06M-C100

Note 2. SWO is muxed with SPI_SCLK of RiBUS. By default, SWO is disconnected.

To enable it, please remove R27 and short R30.

10.8 RiBUS

Any size of the Riverdi EVE4 series display can be connected through RiBUS to act as a slave module to the mainboard.

PIN	SYMBOL	DESCRIPTION	NOTE
1	VCC_+3.3V	Supply voltage for module; TYP3.3V	
2	GND	Ground	
3	SPI_SCLK	SPI SCK signal	
4	MISO/ IO.1	SPI MISO signal / SPI Quad mode: SPI data line 1	

5	MOSI/ IO.0	SPI MOSI signal / SPI Quad mode: SPI data line 0	
6	CS	SPI chips select signal	
7	INT	Interrupt signal from device to the system, Active Low	
8	RST/PD	Reset / Power down signal, Active Low,	
9	GPIO.0	GPIO.0	
10	DISP_AUDIO	Display audio in/out	
11	GPIO.1/IO.2	SPI Single/Dual mode: General purpose IO0. QSPI mode: SPI data line 2	
12	GPIO.2/IO.3	SPI Single/Dual mode: General purpose IO1. QSPI mode: SPI data line 3	
13	NC	Not connected	
14	NC	Not connected	
15	NC	Not connected	
16	NC	Not connected	
17	BLVDD	Supply voltage for backlight	Note 2
18	BLVDD	Supply voltage for backlight	Note 2
19	BLGND	Backlight Ground, internally connected to GND	
20	BLGND	Backlight Ground, internally connected to GND	

Note 1. Matched Riverdi FFC accessory for RiBUS: FFC0520150

Note 2. If any of the Riverdi EVE4 display series (3.5", 4.3", 5.0", or 7.0") is connected through RiBUS, the power supply voltage VDD_IN should be between 6.0V-48.0V. To set up the backlight voltage (BLVDD) at 5.0V, short the pads of the solder bridge labeled 'BLVDD INT'.

If any of the Riverdi EVE4 10.1" display series is connected through RiBUS, the power supply voltage VDD_IN must range between 7.0V-14.0V. To set up the backlight voltage (BLVDD) between 7.0V-14.0V, short the pads of the solder bridge labeled 'BLVDD EXT'

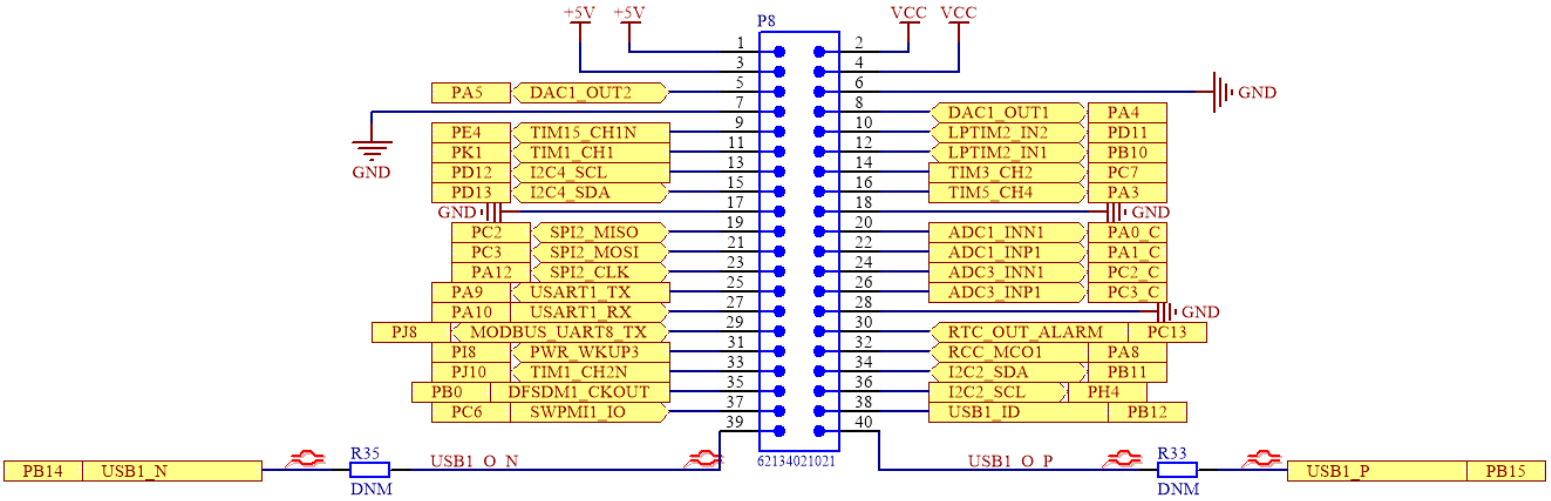
10.9 Expansion Connector

The main board has one 1.27mm, 40-pin expansion pin header which is labeled as “EXPANSION CONNECTOR’ (P8).

It provides direct access to below GPIOs of MCU STM32H757XIH6, which make it possible to easily extend a daughterboard for a specific application.

- 2 x I²C
- 1 x UART
- 1 x USART
- 1 x SPI
- 1 x USB
- 7 x PWMs
- 2 x DACs (Digital-to-analog)
- 2 x ADCs (Analog-to-digital)

Each of the GPIO pins can be configured by software as output (push-pull or open-drain, with or without pull-up or pull-down), as input (floating, with or without pull-up or pull-down) or as peripheral alternate function. Most of the GPIO pins are shared with digital or analog alternate functions. Please refer to the datasheet of STM32H757XIH6 for more details.



I/O/P	PIN	No.	No.	PIN	I/O/P
P	VCC_+5V	1	2	VCC_+3.3V	P
P	VCC_+5V	3	4	VCC_+3.3V	P
I/O	PA5	5	6	GND	P
P	GND	7	8	PA4	I/O
I/O	PE4	9	10	PD11	I/O

I/O	PK1	11	12	PB10	I/O
I/O	PD12	13	14	PC7	I/O
I/O	PD13	15	16	PA3	I/O
P	GND	17	18	GND	P
I/O	PC2	19	20	PA0_C	I/O
I/O	PC3	21	22	PA1_C	I/O
I/O	PA12	23	24	PC2_C	I/O
I/O	PA9	25	26	PC3_C	I/O
I/O	PA10	27	28	GND	P
I/O	PJ8	29	30	PC13	I/O
I/O	PI8	31	32	PA8	I/O
I/O	PJ10	33	34	PB11	I/O
I/O	PB0	35	36	PH4	I/O
I/O	PC6	37	38	PB12	I/O
I/O	PB14	39	40	PB15	I/O

Note 1. The total output current I@5.0V from pin 1 and pin3 is maximum 1A.

The total output current I@3.3V from pin 2 and pin 4 is maximum 500mA.

Note 2. The 'USR LED' is connected to pin 33, PJ10, of the expansion connector.

By default, the resistor R60, (0402, 1k ohms) is soldered. Please remove R60 to use GPIO channel PJ10.

Note 3. Push button BTN2(S2) is connected to pin 35, PB0. Push button BTN1(S1) is connected to pin 37, PC6. To use GPIO channel PB0 and PC6, R57 and R58 should be removed. Please refer to the schematic of subchapter 11.1.

10.10 Micro SD card slot

The mainboard is equipped with Micro-SD slot, which supports all types of Micro SD cards.

10.11 2 x 20-pin, 1.27 mm pin sockets for POE Add-on Board

The 2 x 20-pin, 1.27 mm, pin sockets, labeled as U9a and U9b, are used to connect the Riverdi POE Add-on Board.

The Riverdi POE Add-on Board features 10/100M Ethernet Port with Power-Over-Ethernet enabled. It allows you to power the module through the Ethernet port.

Note. The Riverdi POE Add-on Board is offered as an accessory.

10.12 Battery coin cell holder

The battery coin cell, labeled as BT1, is suitable for CR1220 (Ø12.5 x 2.0mm) Coin Cell Batteries.

The BT1 is connected to the VBAT pin of STM32H757XIH6, which allows to retain the content of the backup registers, SDRAM, and to keep running RTC when the VDD_IN is turned off and unplugged.

10.13 EEPROM

EEPROM address is (0x57 <<1).

11. USER INTERFACES

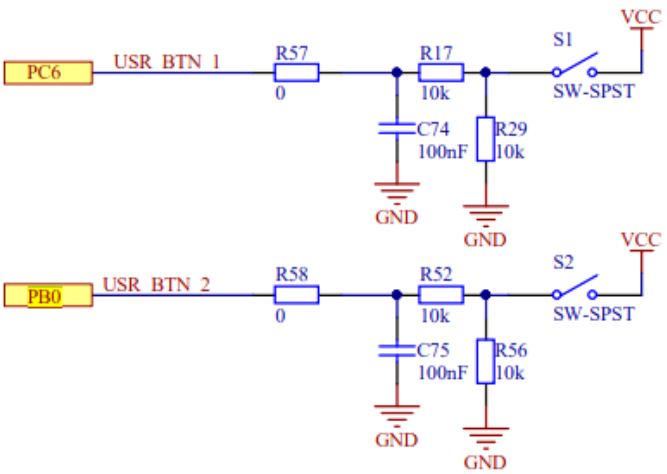
11.1 3 x push buttons

Push button labeled as “RST” is used to “RESET” the module.

Another 2 push buttons labeled as BTN1, BTN2 are for user’s development.

- BTN1(S1) is connected to pin 37, PC6, of the expansion connector.
- BTN2(S2) is connected to pin 35, PB0, of the expansion connector.

By default, pushbuttons BTN1(S1), BTN2(S2) are enabled. To use GPIO channel PC6 and PB0, R57 (0402, 0 ohm) and R58 (0402, 0 ohm) should be removed.



11.2 3 x LEDs

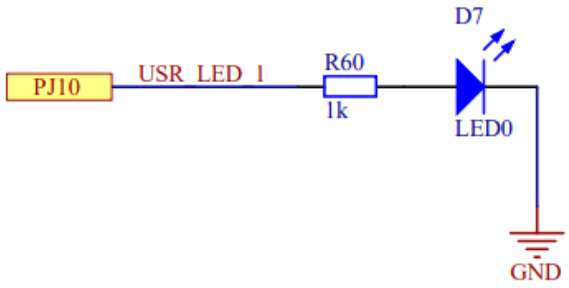
1 x LED, labeled as ‘PWR LED’, emits green light when the modules is powered.

1 x LED, labeled as ‘USB OVR’, emits red light when VCC_USB pin overcurrent occurs.

1 x LED, labeled as ‘USR LED’, is for user’s development.

- The ‘USR LED’ is connected to pin 33, PJ10, of the expansion connector.

By default, the resistor R60, (0402, 1k ohms) is soldered. Please remove R60 to use GPIO channel PJ10.



12. DISPLAY SPECIFICATION

The TFT of the module applies Riverdi high brightness, IPS, 7.0” MIPI TFT : RVT70HSMNWC00

For detailed information of the display, please refer to datasheet of display.

13. INSPECTION

Standard acceptance/rejection criteria for TFT module according to document available here.

14. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	Note 1
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	Humidity 40℃, 90%RH, 120Hrs	
6	Thermal Cycling Test (No operation)	-20℃ for 30mins, 70℃ for 30 mins. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)	
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.

Note 2. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

15. LEGAL INFORMATION

CE marking is usually obligatory only for a complete end product. Riverdi display modules are semi-finished goods which are used as inputs to become part of the finished products.

Therefore, Riverdi display modules are not CE marked.

This is not a standalone product. It was designed as an electronic component. It needs integration with a whole system to be fully functional.

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

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16. CONTACT

Your Success is Our Priority

Stuck on a technical challenge? Need expert guidance?
Our dedicated support team is just a message away.

Contact us at
contact@riverdi.com

We're here to ensure your project's success, every step of the way!