



CHANGELOG

STM32 EMBEDDED 12.1" DISPLAY

Rev.1.0
2024-07-10

This document is applied for the Riverdi STM32 embedded 12.1" series, the display shares the same board as embedded series 10.1":

PRODUCT NAME	DESCRIPTION
RVT121HVSNNWN00	IPS, 12.1", 1280x800, 1000cd/m ² , STM32, no touch panel,
RVT121HVSFWN00	IPS, 12.1", 1280x800, 1000cd/m ² , STM32, no touch panel, metal frame
RVT121HVSNNWC00-B	IPS, 12.1", 1280x800, 850cd/m ² , STM32, uxTouch, optical bonding
RVT101HVSFWCA0-B	IPS, 12.1", 1280x800, 850cd/m ² , STM32, aTouch, optical bonding, metal frame
RVT101HVSNNWCA0-B	IPS, 12.1", 1280x800, 850cd/m ² , STM32, aTouch, optical bonding



1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2024-07-10	First Release	



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Revision information

Riverdi embedded 12.1" STM32H7 series share the same board with 10.1" embedded STM32H7 series. 12.1" STM32H7 series applies PCB_101STM32H7_Rev1.5 from the beginning. The current PCB board revision is PCB_101STM32H7_Rev1.6.

3. PCB Rev. 1.6

- Added LSE X2, C86, C87 for RTC working
- Connected USR_BTN2 to BOOT0 of MCU, removed boot resistor(R50)
- Changed 1.27mm select jumpers to backlight soldering bridges
- Added ESD diode(D20) on SWD
- Added R66(DNM by default) to connect the RXD pin of CAN FD1 to PI8 of MCU
- Standby pin of CAN_FD1 is connected to PB1 of the MCU
- Added R50, R58 to select function between USB1_OVERCURRENT and RiBUS GPIO.0
- Added R61, R62 to select function between CTP_RST and RiBUS GPIO.1
- Changed USR_BTN2 resistor R52(10k->1k, R56(DNM))

4. PCB Rev. 1.5

- Connected PB12 USB ID to USB molex connector (actual is PC0)
- Added series resistor pin of SIC473
- Added C117 capacitor