



CHANGELOG

STM32 EMBEDDED 10.1" DISPLAY

Rev.1.0
2024-06-25

This document is applied for the Riverdi STM32 embedded 10.1" series:

PRODUCT NAME	DESCRIPTION
RVT101HVSNWN00	IPS, 10.1", 1280x800, 1000cd/m ² , STM32, no touch panel,
RVT101HVSFWN00	IPS, 10.1", 1280x800, 1000cd/m ² , STM32, no touch panel, metal frame
RVT101HVSNWC00-B	IPS, 10.1", 1280x800, 850cd/m ² , STM32, uxTouch, optical bonding
RVT101HVSNWC00	IPS, 10.1", 1280x800, 800cd/m ² , STM32, uxTouch, air bonding
RVT101HVSNWCA0	IPS, 10.1", 1280x800, 800cd/m ² , STM32, aTouch, air bonding
RVT101HVSFWCA0	IPS, 10.1", 1280x800, 800cd/m ² , STM32, aTouch, air bonding, metal frame



1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2024-06-25	First Release	



2. CONTENTS

- 1. REVISION RECORD..... 2
- 2. CONTENTS..... 3
- 3. PCB Rev. 1.6..... 4
- 4. PCB Rev. 1.5..... 4
- 5. PCB Rev. 1.4..... 4
- 6. PCB Rev. 1.3..... 4
- 7. PCB Rev. 1.2..... 4
- 8. PCB Rev. 1.1..... 4



3. PCB Rev. 1.6

- Added LSE X2, C86, C87 for RTC working
- Connected USR_BTN2 to BOOT0 of MCU, removed boot resistor(R50)
- Changed 1.27mm select jumpers to backlight soldering bridges
- Added ESD diode(D20) on SWD
- Added R66(DNM by default) to connect the RXD pin of CAN FD1 to PI8 of MCU
- Standby pin of CAN_FD1 is connected to PB1 of the MCU
- Added R50, R58 to select function between USB1_OVERCURRENT and RiBUS GPIO.0
- Added R61, R62 to select function between CTP_RST and RiBUS GPIO.1
- Changed USR_BTN2 resistor R52(10k->1k, R56(DNM))

4. PCB Rev. 1.5

- Connected PB12 USB ID to USB moxex connector (actual is PC0)
- Added series resistor pin of SIC473
- Added C117 capacitor

5. PCB Rev. 1.4

- Connected overcurrent output of USB switch to PC15
- Added mux resistors to PB3 to get SWO, R47, R48
- Added series resistor on OSC both for 10 and 7"
- Changed D5 to SSB44HE3
- Added R50, boot mode
- Added R49, improve EMC performance

6. PCB Rev. 1.3

- Added ESD to expansion connector
- Added common mode filter on power input
- Added battery
- Added series resistor on USB1_Overcurrent and USB1_EN
- Changed R43 to 47K
- Added PWR EN to LAN addon
- Added BOOT near to CPU

7. PCB Rev. 1.2

- Changed FFC length for PCAP connection
- Changed power moxex to 6-pin
- Moved C22 from QSPI

8. PCB Rev. 1.1

- Changed CANbus connector to 4-pin + power supply
- Changed P5 from THT to SMD
- Changed STM32 external quartz to oscillator
- Added programming pads
- Added diode on 12V from POE



- Added more thermal pads under DC/DC converters
- Added EN pin to power supply Molex socket