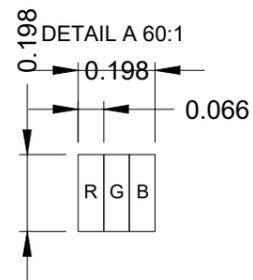
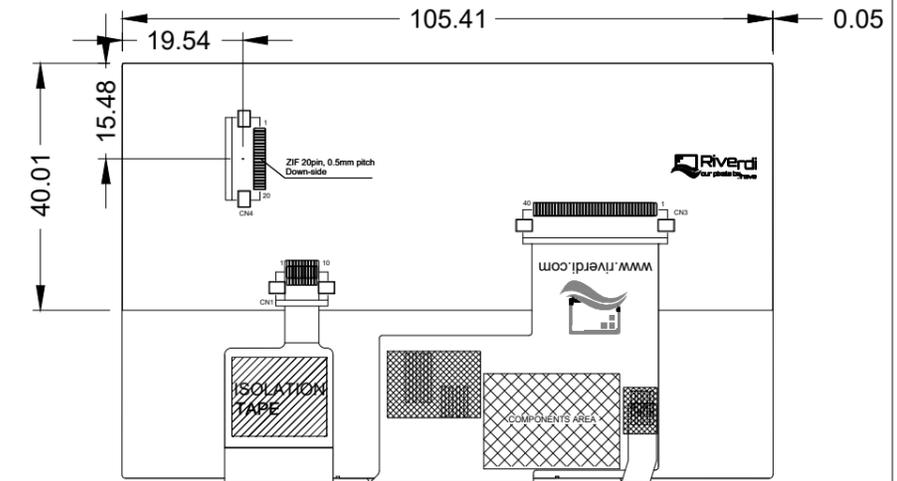
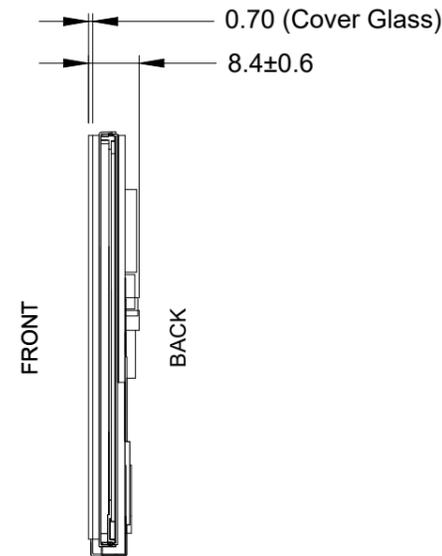
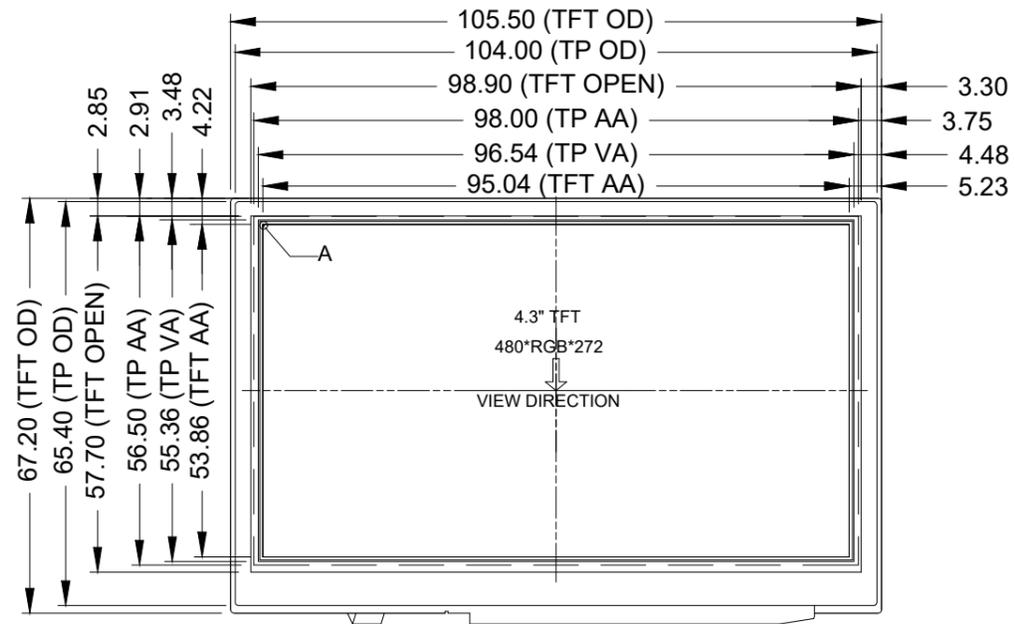
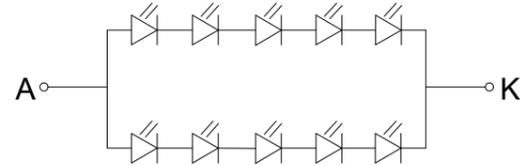


PIN	DESC
1	VDD
2	GND
3	SPI_SCLK / I2C_SCL
4	MISO / I2C_SDA
5	MOSI / I2C_SA0
6	CS / I2C_SA1
7	INT
8	PD
9	MODE
10	AUDIO_OUT
11	NC
12	NC
13	NC
14	NC
15	NC
16	NC
17	BLVDD
18	BLVDD
19	BLGND
20	BLGND



INTERNAL BACKLIGHT CIRCUIT DIAGRAM



- NOTES:
- DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
 - RESOLUTION: 480x272
 - VIEWING DIRECTION: 12 O'CLOCK
 - IC CONTROLLER: FT801
 - OPERATING TEMP.: -20°C ~ 70°C
 - STORAGE TEMP.: -30°C ~ 80°C
 - LED BACKLIGHT: 10-LED WHITE
 - SURFACE LUMINANCE: 500 cd/m²
 - OPERATING VOLTAGE: 3.3V
 - GENERAL TOLERANCE: ±0.2
 - RoHS COMPLIANT

Ver.	DESCRIPTION	DATE
3.1	Dimension overhaul	2018.10.22
3.0	New IC CTP controller.	2018.04.09
2.0	Glass change, new TFT open dimension, FFC shape. New IC controller and updated pinout.	2017.05.29

CUSTOMER APVL		DATE	2018.10.22
DRAWN	SCALE	TITLE	
DFTG CHK	UNIT	RVT4.3B480272CNWC81	
ENGR CHK	mm	MODEL	
APPROVAL		DWG NO	PAGE
		Rev. 3.1	1/1