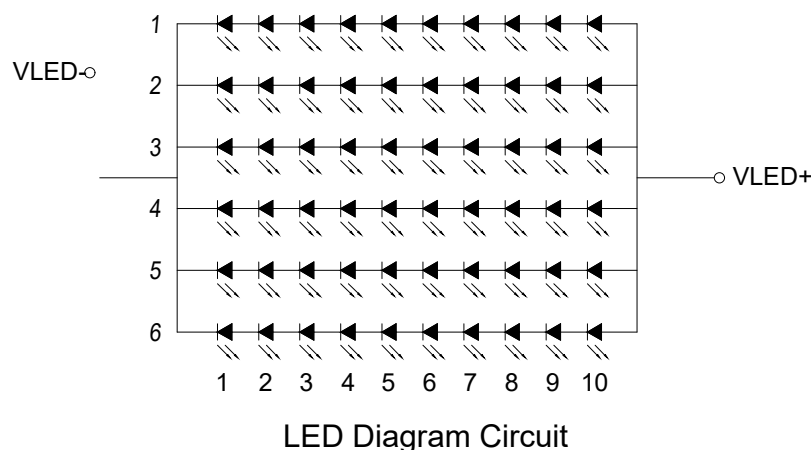
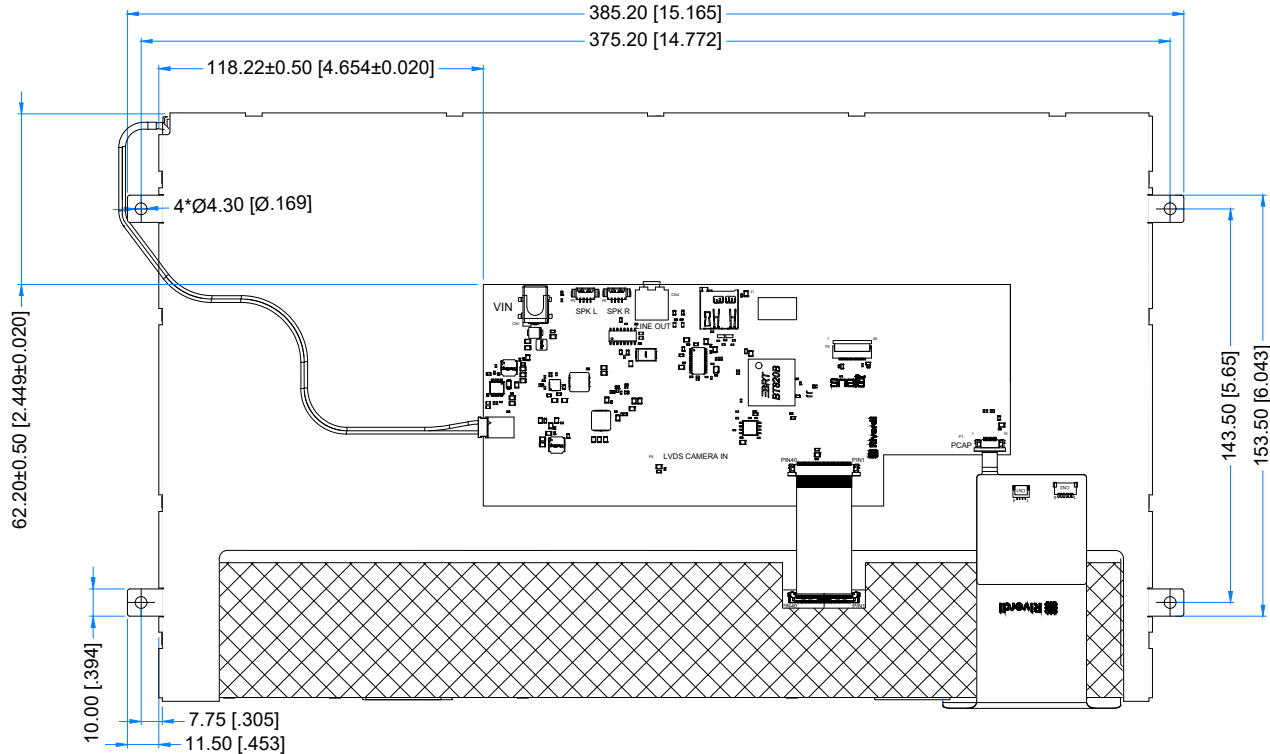
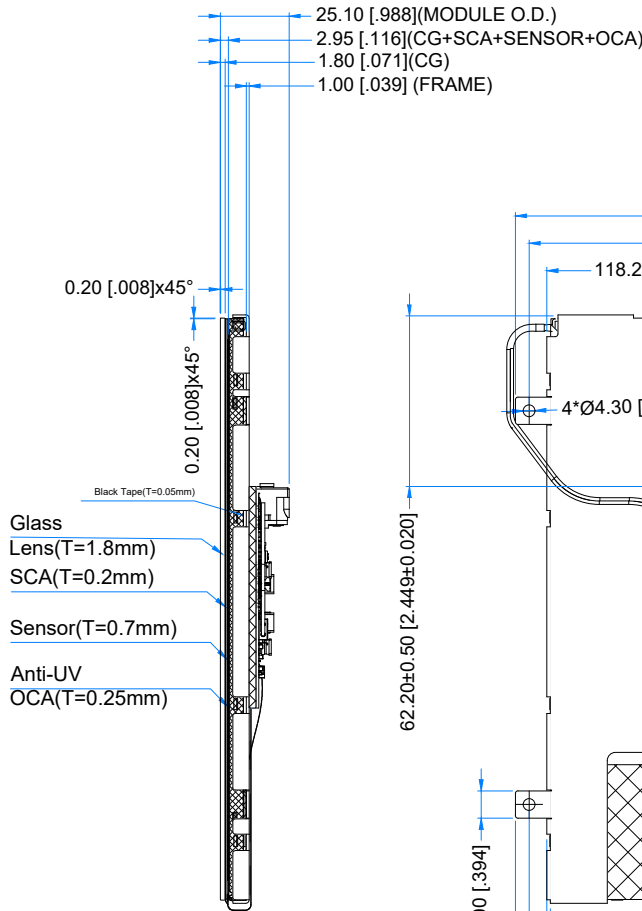
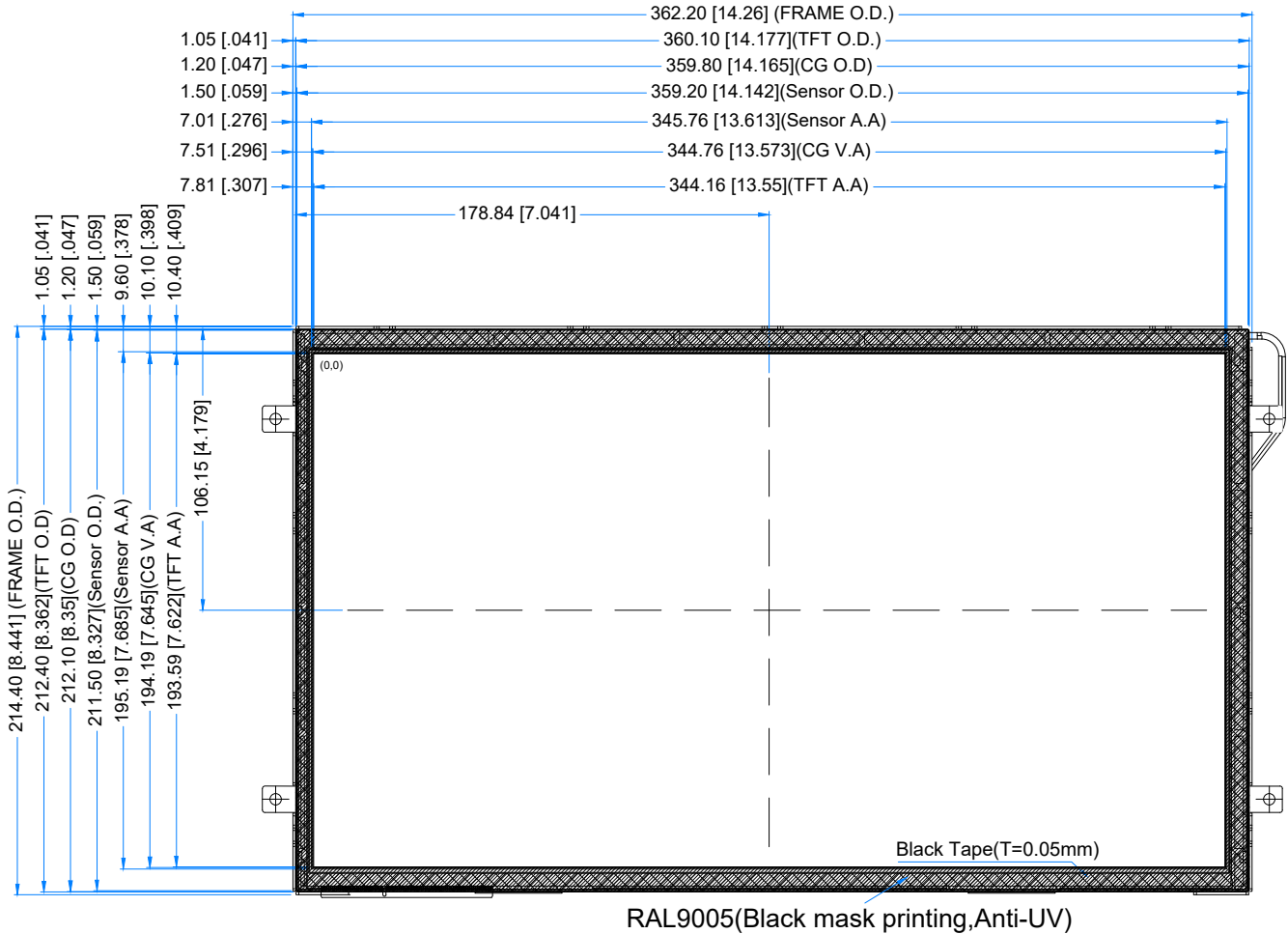


Revision:	Changes:	Date:
1.0	Initial Case	2025.07.04



PINOUT ON THE 2ND PAGE

TFT NOTES: 1. LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS 2. RESOLUTION: 1920 x 1080 3. VIEWING ANGLE: FREE 4. DRIVING IC ON THE BOARD: BT820B 5. INTERFACE: SPI/QSPI 6. DRIVING VOLTAGE: 6.0V	TP NOTES: 1. TP STRUCTURE: G+G 2. CG THICKNESS: 1.8mm 3. DRIVER IC: ILI2510 4. INTERFACE: USB, I2C 5. OPERATING VOLTAGE: 3.3V(CTP_I2C); 5.0V (CTP_USB)	GENERAL NOTES: 1. MODULE SURFACE LUMINANCE: 850 cd/m ² 2. OPTICAL BONDING 3. OPERATING TEMPERATURE: -20°C ~ 70°C 4. STORAGE TEMPERATURE: -30°C ~ 80°C 5. WITHOUT INDIVIDUAL TOLERANCE: ±0.3mm 6. RoHS COMPLIANT					
			PN: RVT156HKBFWCA0-B				
			SN:				
			DRAWN: M.Stabinski		2025.07.04	1:2.76	
			CHECKED: M.Wierzbowski		2025.07.04	[mm]	
APPR:			ISO A3	P. 1 of 1			



LCD CONNECTOR			
Pin No.	Desc.	Pin No.	Desc.
1	-	11	GPIO.1/IO.2
2	GND	12	GPIO.2/IO.3
3	SPI_SCLK	13	NC
4	MISO/IO.1	14	NC
5	MOSI/IO.0	15	NC
6	CS	16	NC
7	INT	17	-
8	RST/PD	18	-
9	GPIO.0	19	BLGND
10	DISP_AUDIO	20	BLGND

POWER JACK	
Pin No.	Desc.
1	VSUPPLY
2	GND

A diagram of a power jack connector. It consists of a central black circle representing the positive terminal, flanked by two white circles representing the negative terminals. The left white circle is labeled '2' and the right white circle is labeled '1'. Lines connect the central black circle to both the '2' and '1' circles.